



Attorney's Docket No.: 10559-095001 / P7611

2142#

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Mark L. Skarpness Art Unit : 2142
Serial No. : 09/469,960 Examiner : Douglas B. Blair
Filed : December 21, 1999
Title : PRIORITIZING DATA TRANSFERS OVER DATA BUSES

Commissioner for Patents
Washington, D.C. 20231

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Technology Center 2100

TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	20	-	20	=	0	\$0
Independent	5	-	3	=	2	\$168
First Presentation of Multiple Dependent Claims						\$0

TOTAL FEE DUE \$168

A check for \$168 is attached.

Respectfully submitted,


ALEX CHEN
REG NO 45,591

Scott C. Harris
Reg. No. 32,030

Date: 1/9/03
Fish & Richardson P.C.
4350 La Jolla Village Drive, Suite 500
San Diego, California 92122
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10243680.doc

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

January 9, 2003

Date of Deposit

Signature

Carroll Allman
Typed or Printed Name of Person Signing Certificate



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RESPONSE

In response to the action mailed October 9, 2002, please amend the application as follows:

In the claims:

Please cancel claims 1, 2, 11 and 12.

Please amend claims 3-10 and 13-19 as follows:

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- SUB
B3
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3. (Amended) A computer system comprising:
a host processor;
a peripheral device configured to transfer data to the host processor over an attachment bus using at least first and second types of data transfers, comprising:
a classifying circuit configured to separate the data into a first class associated with the first type of transfer and a second class associated with the second type of transfer;

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